

Fig. 1

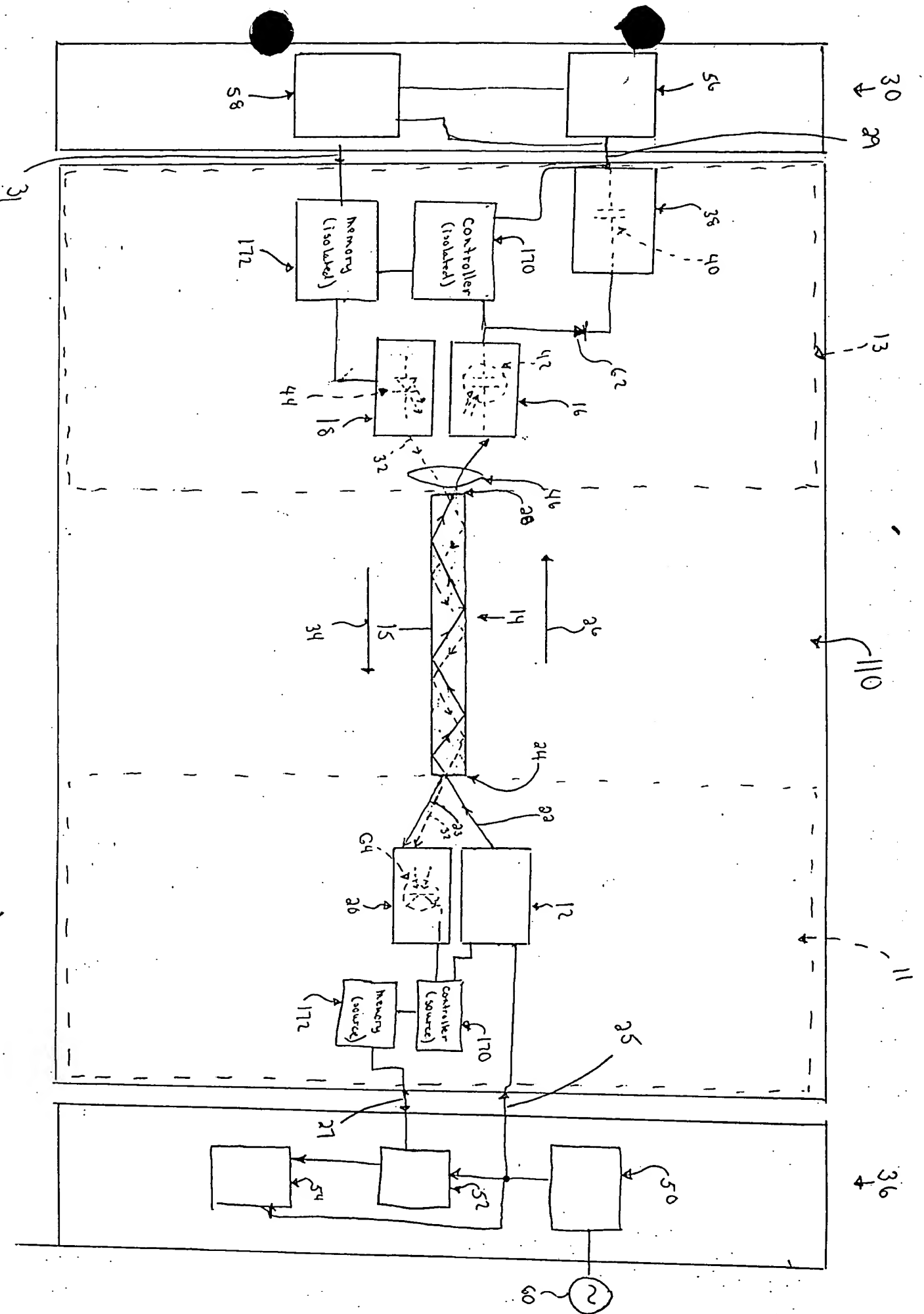


Fig. 2

210

30

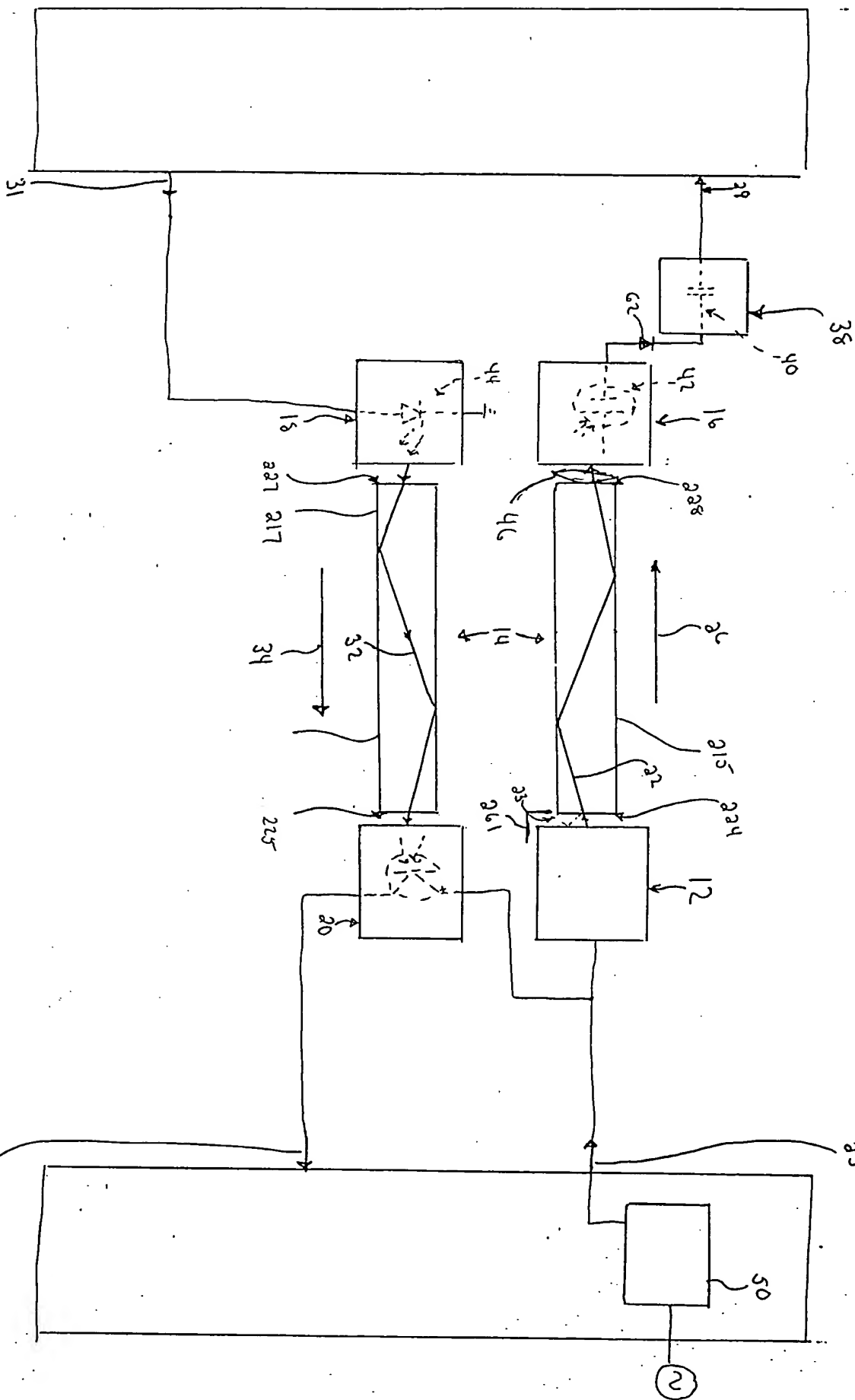


Fig. 3

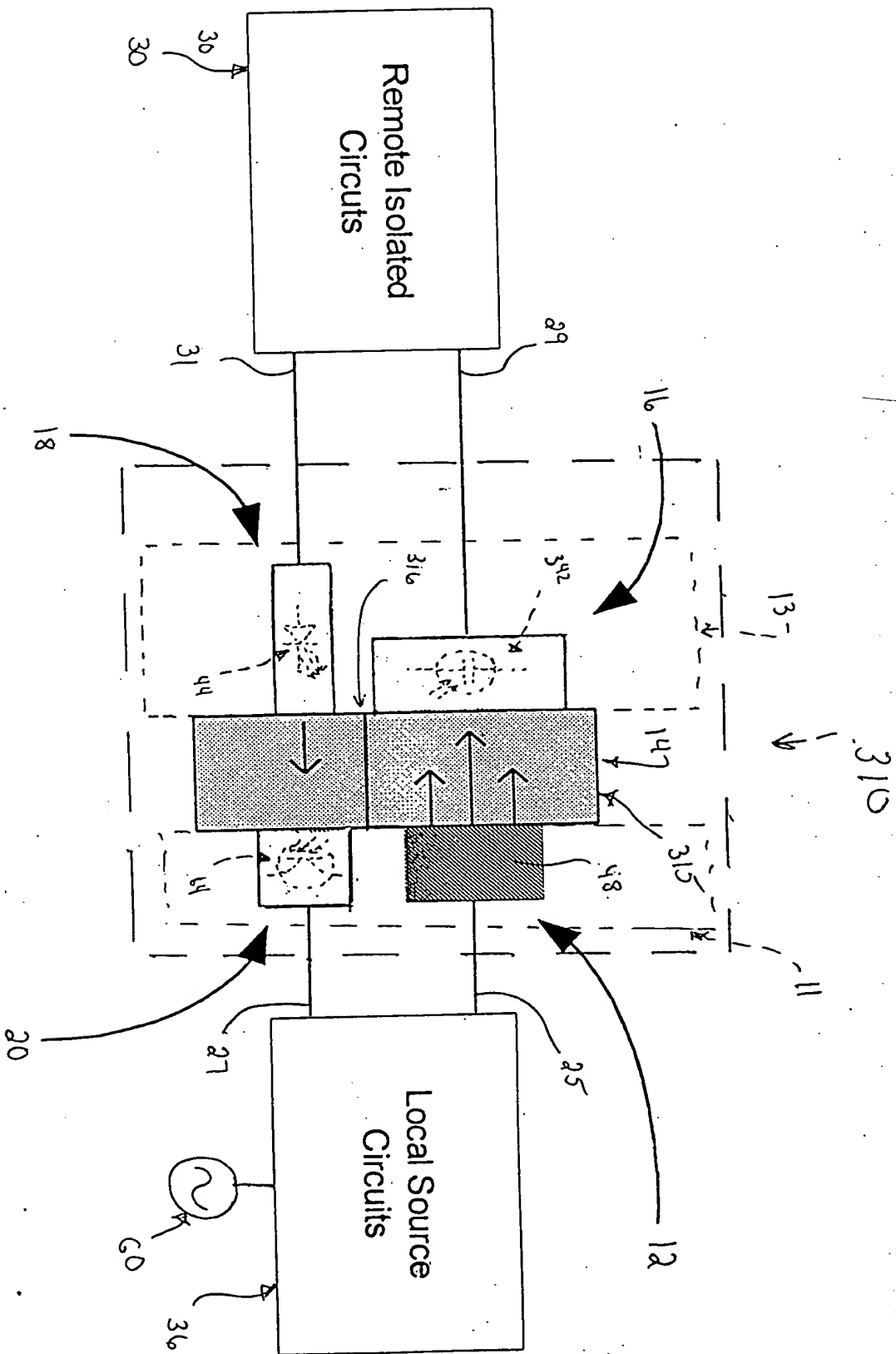


Fig 4.

FIG. 4 is a block diagram of a system in accordance with the present invention. The system includes a local source circuit 20 and a remote isolated circuit 30. The local source circuit 20 is connected to a central bus 31 via a connection point 25. The remote isolated circuit 30 is connected to the central bus 31 via a connection point 29. The central bus 31 includes several interface blocks 31a, 31b, 31c, and 31d. Signal flow is indicated by arrows 12, 13, 14, 16, 18, 20, 27, 36, 40, 44, and 48. A dashed line 11 and a dashed line 13-1 are also shown.

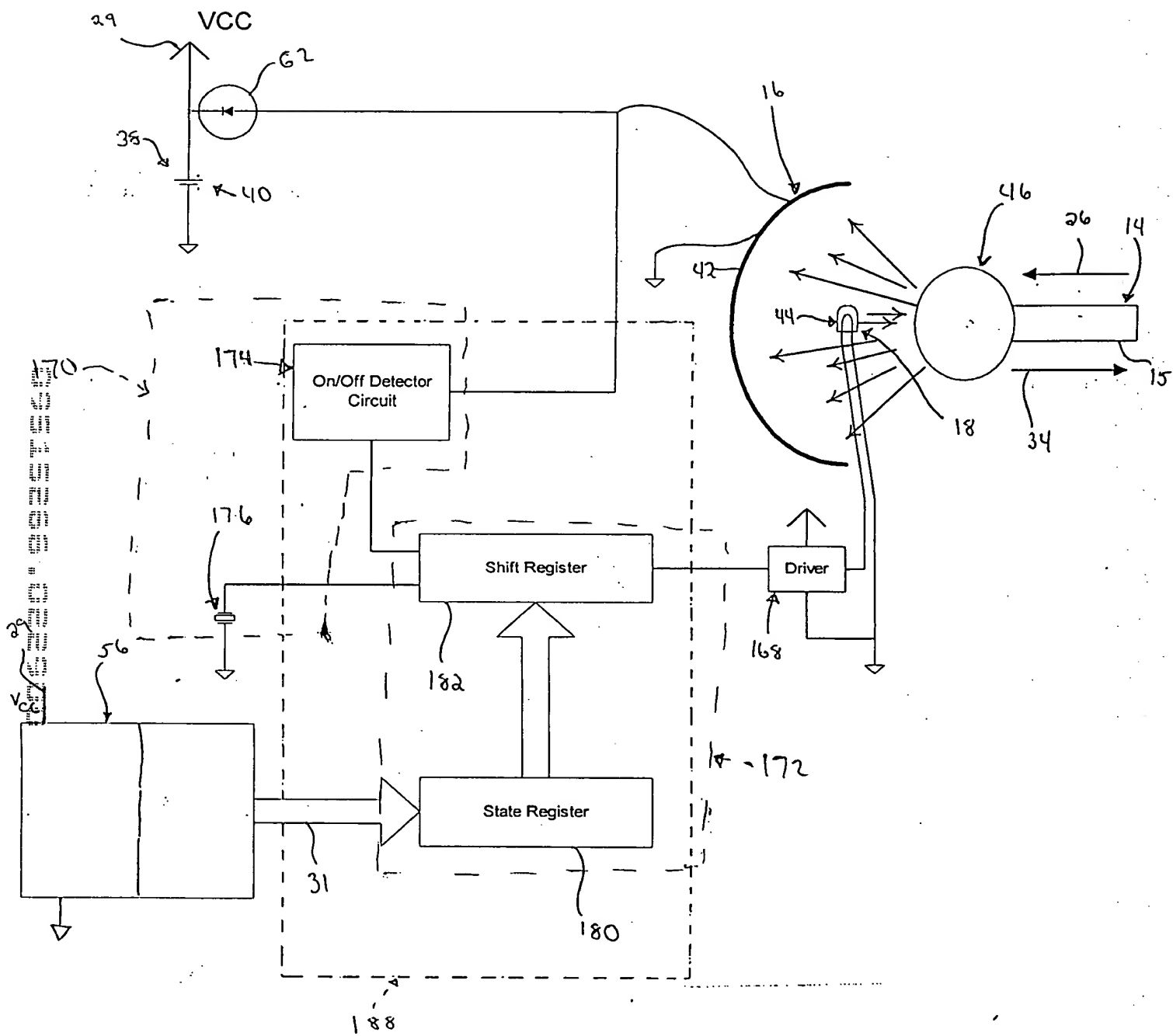


Fig. 5

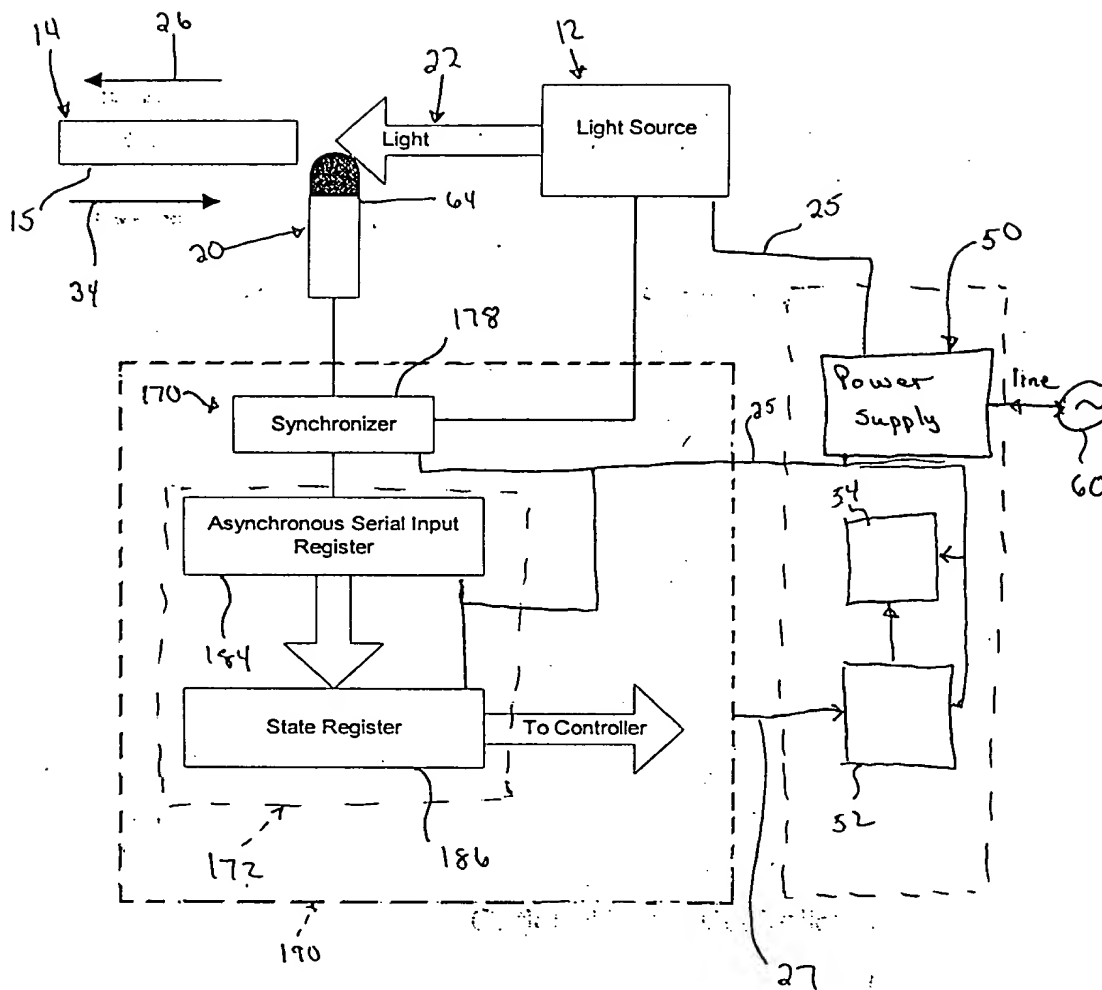


Fig. 6

